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*Embedded Memories for Nano-Scale VLSIs*

In modern microprocessors and systems-on-a-chip, the embedded memory system plays a key role in determining the design's overall performance, power, area, reliability, and yield. As fabrication process technologies scale into the deep nanometer regime, increasing device variability poses particular difficulties for memory design due to the large number of variation-sensitive near minimum ...

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SYDNEY: Weebit Nano Ltd has appointed non-volatile memory design expert Mr Ilan Sever as Vice President Research & Development to drive the Company's progress within the embedded, standalone and neuromorphic markets, a news release said. Mr Sever will commence in the role on 24November 2020. Mr Sever brings more than 25 years' in Very Large-Scale Integration (VLSI) design to Weebit Nano ...

*Weebit Nano appoints Ilan Sever to drive R&D | NewsnReleases*

thereof. For quantum memories, and quantum computing devices, |0 and |1 typically correspond to states of two different energies in either a natural "atomic system" (e.g. ion traps [27], NV centres in diamond [60], neutral atoms [8] or atomic ensembles [51]), or artificially designed nano-scale systems (e.g. superconducting quan-

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Listed semiconductor company Weebit Nano (ASX: WBT) has appointed non-volatile memory design expert Mr Ilan Sever as Vice President Research & Development to drive the company's progress within the embedded, standalone and neuromorphic markets. Mr Sever brings more than 25 years' in Very Large-Scale Integration (VLSI) design to Weebit Nano with extensive experience across the entire ...

*Weebit Nano appoints memory design expert Ilan Sever as VP ...*

Fully autonomous nano-scale unmanned aerial vehicles (UAVs) are be?tting embodiments for this class of smart ... [12] ?25 : ?0.5 ?50 Embedded nano-size [13] ?10 : ?0.01 ?5 MCU example, in this IoT scenario, a relevant application for intel-pico-size [14] ?2 : 0.001 ?0.1 ULP ... workloads on a heavily power- and memory-constrained de ...

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